Homework 5
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Due October 30th, 2009

1 Karnaugh Map Minimization and VHDL

Draw the truth table, Karnaugh Map, and the VHDL code for each canonical expression.

1. \( Z(A, B, C, D) = \Pi(0, 4, 6, 8, 12) \)

2. \( Y(A, B, C, D) = \Sigma(0, 1, 3, 13) \)