Homework 4
Matthew Shuman

Latches and Flip-Flops
1. 3.35 in the text.
2. 3.38 in the text.

Sequential Timing
3. Problem 3.34 in the text.

Finite State Machines
4. Develop a state machine for a 3 bit up down counter. Include all 7 steps of the state machine process. Note that a 3 bit up down counter has one input that controls whether the counter increments or decrements. Also include an asynchronous reset to clear the count to zero.

Interview Questions
5. Problem 3.5
6. Problem 3.6
7. Problem 3.9