Homework 2

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Due April 18th @ 4 PM, 2012

Problems are from the ECE 271 textbook.

1 Truth Tables, Boolean Logic, Canonical Forms, Logic Mini-
    mizations, and NAND/NOR Implementation
    2.1, 2.2, 2.3(a-d), 2.5(a-d), 2.6(a-d)

2 Spring Picnics
    2.22

3 Combinational Logic Design
    2.23, 2.24

4 Timing
    2.32 - 2.34

5 Interview Questions
    2.3

Extra Credit Points: (1 point each)
In the very center of Figure 32 (page 44) of the Tiny26 data sheet there is a tristate buffer. Explain how that buffer controls whether pins are configured as inputs or outputs in the tiny26.

In the very center top of Figure 32 (page 44) of the Tiny26 data sheet there is a three input AND gate. Explain how that gate controls whether a pull up resistor is enabled on that input/output of the tiny26.