ECE441
SENIOR DESIGN PROJECT

Term: Fall 2013 / Winter 2014 / Spring 2014
Text: Design for Electrical and Computer Engineers
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COURSE OVERVIEW

ECE441/2/3 is the Electrical Engineering capstone design sequence. The course is intended to follow the engineering design model shown. The cycle is divided out over the various terms with the intention of a finished fully tested project being ready for the engineering expo. Research projects are also possible using the same model.

During ECE441, we will be describing the system level architecture. Once the top-level design is completed, we will continue to design the sub-blocks based on our specifications. While it will not be required, some basic implementation of the individual blocks is suggested as it will help create the design.

In ECE442, we will finish the implementation of the sub-blocks and after each one has been implemented and validated it will be integrated into the system level. Complete validation will not be needed until ECE443, but some of the system requirements will need to be validated during ECE442.

Finally in ECE443, we will finish out the project passing all system validation tests and presenting projects at the Engineering Expo.

It is important to remember that success in this course is your responsibility. Do not depend on the faculty advisor, sponsor, or mentor to keep your project on schedule. Advisors and mentors will support and guide you in completing your project successfully, but you must take the initiative and seek out their help. A successful project is worth your effort and provides a tangible example of your capabilities to potential employers.

COURSE LEARNING OUTCOMES

At the completion of the courses, students will be able to perform the following tasks:

1. Write a concise project description stemming from an identified objective (ABET Outcomes: E, G, a, c, f)
2. Collect and review technical information on a project from relevant external resources (ABET Outcomes: I, J, e)
3. Identify and describe the constraints on projects imposed by resources and in terms of broader impact (ABET Outcomes: C, E, H, a, d, f, m)
4. Identify project milestones (ABET Outcomes: G, d)
5. Acquire tooling and hardware (components) for a breadboard / prototype (ABET Outcomes: K, j)
6. Actively revise and adjust solutions based on new information (ABET Outcomes: B, C, E, I, K, m)
7. Record technical results, and measure progress (ABET Outcomes: G, a, d)
8. Complete (from design to at least the prototype) a significant ECE project (ABET Outcomes: A, B, C, E, G, K, M, d, i)
9. Generate Operational and Technical Documentation for an ECE project (ABET Outcomes: G, a, b, c, d)
10. Present project information succinctly to a technically aware audience (ABET Outcomes: A, G, f)
11. Work effectively in professional multidisciplinary teams utilizing appropriate communication skills (ABET Outcomes: D, G, f)
12. Analyze ethical dilemmas in terms of the impact of engineering solutions in global, economic, environmental, social context (ABET Outcomes: F, H, J)

WIC Requirements:
1. Develop and articulate content knowledge and critical thinking in the discipline through frequent practice of informal and formal writing.
2. Demonstrate knowledge/understanding of audience expectations, genres, and conventions appropriate to communicating in the discipline.
3. Demonstrate the ability to compose a document of at least 2000 words through multiple aspects of writing, including brainstorming, drafting, using sources appropriately, and revising comprehensively after receiving feedback on a draft.

Academic Dishonesty

At Oregon State University academic dishonesty is defined by the Oregon Administrative Rules 576-015-0020.1.a-c as: 
An intentional act of deception in which a student seeks to claim credit for the work or effort of another person or uses unauthorized materials or fabricated information in any academic work.

Academic dishonesty includes:

- **CHEATING** - use or attempted use of unauthorized materials, information or study aids or an act of deceit by which a student attempts to misrepresent mastery of academic effort or information. This includes unauthorized copying or collaboration on a test or assignment or using prohibited materials and texts.

- **FABRICATION** - falsification or invention of any information (including falsifying research, inventing or exaggerating data and listing incorrect or fictitious references.

- **ASSISTING** - helping another commit an act of academic dishonesty. This includes paying or bribing someone to acquire a test or assignment, changing someone's grades or academic records, or taking a test/doing an assignment for someone else (or allowing someone to do these things for you). It is a violation of Oregon state law to create and offer to sell part or all of an education assignment to another person (ORS 165.114).

- **TAMPERING** - altering or interfering with evaluation instruments and documents.

- **PLAGIARISM** - representing the word or ideas of another person as one's own OR presenting someone else's words, ideas, artistry or data as one's own. This includes copying another person's work (including unpublished material) without appropriate referencing, presenting someone else's opinions and theories as one's own, or working jointly on a project, then submitting it as one's own.

IEEE Code of Ethics

As a community of Electrical and Computer Engineers, we have a duty to present ourselves and our profession to each other and the public in the best light possible. The IEEE has a code of Ethics that should always be considered. It reads:

“We, the members of the IEEE, in recognition of the importance of our technologies in affecting the quality of life throughout the world, and in accepting a personal obligation to our profession, its members and the communities we serve, do hereby commit ourselves to the highest ethical and professional conduct and agree:

1. to accept responsibility in making decisions consistent with the safety, health and welfare of the public, and to disclose promptly factors that might endanger the public or the environment;
2. to avoid real or perceived conflicts of interest whenever possible, and to disclose them to affected parties when they do exist;
3. to be honest and realistic in stating claims or estimates based on available data;
4. to reject bribery in all its forms;
5. to improve the understanding of technology, its appropriate application, and potential consequences;
6. to maintain and improve our technical competence and to undertake technological tasks for others only if qualified by training or experience, or after full disclosure of pertinent limitations;
7. to seek, accept, and offer honest criticism of technical work, to acknowledge and correct errors, and to credit properly the contributions of others;
8. to treat fairly all persons regardless of such factors as race, religion, gender, disability, age, or national origin;
9. to avoid injuring others, their property, reputation, or employment by false or malicious action;
10. to assist colleagues and co-workers in their professional development and to support them in following this code of ethics."
MAJOR ASSIGNMENTS AND PERCENT OF COURSE GRADE

LATE WORK POLICY
All late work will receive no credit. Only pre-discussed exceptions will be accepted.

PERSONAL BIOGRAPHY (10 POINTS) – INDIVIDUAL
Individually each student must login and completely set up their student portal information. This must include a photo upload, completed biography, mobile phone number, and alternate email address. See assignments online for due date and time.

GOOGLE SITE SETUP AND PROJECT DESCRIPTION (30 POINTS) – GROUP GRADING
WIC: (Informal Written Work)
Your group will need to establish a webpage that will act as a permanent repository of your project design. This webpage will be the primary method of showing your project to people both inside and outside of the university. You must use the Google Sites system for your webpage. Once your group has been assigned, we will add your ONID accounts to the appropriate Google Site projects. See assignments online for due date and time.

Scoring: 30pts. - Satisfactory, 0pts. - Unsatisfactory
If your web page is considered unsatisfactory, you will be given until 4pm on Friday of Week 3 to revise it for full credit.

PROJECT SPECIFICATION (TECHNOLOGY REVIEW) (150 POINTS) – INDIVIDUAL GRADING
WIC: (500 words minimum, 7 weeks to revise)
Submitted via TEACH
In this review you will explain what your project is (in your own words, do not simply copy the description from class), what design issues are involved, and what’s been done to date in this area. The primary purpose is to review the ‘state of the art’ and find other projects that you can learn from. To write this report, you will need to conduct library research, patent searches, interview your sponsor, etc. Cite your sources according to IEEE format (see http://standards.ieee.org/guides/style/section7.html for details). The report will be carefully reviewed for technical content and quality of writing by your group’s faculty advisor and project sponsor. See assignments online for due date and time.

PROJECT SPECIFICATION (PRELIMINARY DESIGN) (200 POINTS) – INDIVIDUAL/GROUP GRADING
WIC: (1000 new words minimum, 3 weeks to revise)
Submitted via BeaverSource
For this submission, you will revise your previous work and will brainstorm together to develop a list of possible design solutions along with a brief technical description of the feasibility of each with supporting calculations and analysis as appropriate. From this list of possible solutions, the group will identify a general approach they feel is most appropriate. This is not necessarily a specific design, but rather a general group of similar designs. You must include a system-level block diagram and explain all inputs or outputs clearly. See assignments online for due date and time.

PUBLICITY POINTS (50 POINTS) – GROUP GRADING
WIC: (Informal Written Work)
During the term, the online presence for your project needs to be updated. This process needs to be continually performed or it will affect the publicity points for your group. Examples include contacting sponsors and updating projects and images.

PEER BLOCK REVIEWS (40 POINTS) – INDIVIDUAL GRADING
WIC: (Informal Written Work)
Each student must submit one of the blocks they are responsible for to the online grading system. Another student will be randomly selected to review the block in question for technical merit based on the rubric that will be used in the final grading. For this assignment, each student is graded based on what they submit and how ‘insightfully’ they grade the block they are assigned. No student will have any effect on another student’s grade. See assignments online for due date and time.

Bi-WEEKLY SCRUM UPDATES (160 POINTS) – INDIVIDUAL GRADING
Submitted by 4pm Wednesdays on Weeks 4, 6, 8, and 10
On alternate weeks the mandatory Wednesday meeting in class will be used as a SCRUM planning meeting. These meetings will be based on the SCRUM model and will cover what happened in the previous sprint, examine any new customer stories, and define what will be accomplished over the next sprint.

**READING QUIZZES (50 POINTS) – INDIVIDUAL GRADING**

During each week, there will be a quick quiz covering assigned reading for the week. The quiz is in the first 10 minutes of Wednesday’s classroom session.

**FINAL BLOCK EVALUATION (250 POINTS) – INDIVIDUAL GRADING**

WIC: (500 new words minimum, 3 weeks to revise)

Near the end of Fall term, each student will be responsible for having completed at least of block from the group top-level design. This block must be completely designed and tested including prototyping and interface testing. To receive credit, the corresponding Google Site section must be updated. Electronic copies of all important datasheets must be on Google Site before the review time. They should be linked from each block that uses the datasheet. Datasheets for each silicon or electro-mechanical part must be included. Any ‘special’ components not covered by this statement must also have datasheets included. Additionally, evidence of each block passing its required test must present on Beaversource page for that block.

Under the block testing section of each block, there should be evidence of the block passing if it has successfully done so. This most likely will take the form of one video showing the test as written from beginning to the end. Other mechanisms including photos or scans could be used if they seem MORE appropriate than a video.

Each group will be expected to meet during a specially chosen time. All of the meetings will be in DB211. Please come ready to demonstrate any block tests noted as successful on the Google Site and with the assignment grading sheet filled out and printed.

**PEER REVIEWS (40 Points) – INDIVIDUAL GRADING**

WIC: (Informal Written Work)

Printed copies to Instructor (KEC1148)

All group members will individually prepare a “peer review,” twice during the term using the online peer review tool. In these reviews, students will reflect on their peers’ work.

**PEER REFLECTION (20 Points) – INDIVIDUAL GRADING**

WIC: (Informal Written Work)

Printed copies to Instructor (KEC1148)

All group members will respond to their groups peer reviews in a peer reflection document using the online tool. In these reflections, students will suggest solutions they plan to implement to improve.

**IMPORTANT INFORMATION**

**BUDGET**

Each group will have access to a budget of $300 starting in November. Projects that have an industry sponsor will be awarded $600. If you need additional funding, you will need to locate that on your own or supply it from your own resources.

There are multiple opportunities for free/or sample parts, but beware: YOU GET WHAT YOU PAY FOR. If you need parts from the vendors below, follow the steps to get parts:

Analog Devices
- Go to the link below and fill out the form.
- [https://form.analog.com/Form_Pages/corporate/parts.aspx](https://form.analog.com/Form_Pages/corporate/parts.aspx)

National Semiconductor
- Go to the link below.
- Find your part.
- If there is a sample button, send the part number to Donald Heer.
- If there is not a sample button, the part cannot be sampled.
Texas Instruments

- Enter the TI Analog University Design Contest at the link below. Follow the steps to sample parts.