

[1] F.J. Wang, G.C. Temes and S. Law, "A Quasi-Passive CMOS Pipelined D/A Converter," JSSC, pp. 1752-1755, Dec. 1989.

[2] R.E. Suarez, P.R. Gray and D. Hodges, "All-MOS Charge Redistribution A/D Conversion Techniques - Part II," JSSC, pp. 379-385, Dec. 1975.

[3] P. Rombouts et al., "Capacitor Mismatch Compensation for the Quasi-Passive SC DAC," TCAS-I, pp. 68-71, Jan. 1998.

[4] J. Steensgaard, U. Moon and G.C. Temes, "Mismatch-Shaping Serial D/A Converter," Proc. ISCAS, vol.II, pp. 5-8, May 1999.

[5] P. Chen and T.C. Liu, "Switching Schemes for Reducing Capacitor Mismatch Sensitivity of Quasi-Passive Cyclic DAC," TCAS-II, pp. 26-30, Jan. 2009.