## CS444/544 Operating Systems II

Lecture 15 Lock and Synchronization (cont.) Concurrency Bugs and Deadlock 5/22/2024

Acknowledgement: Slides drawn heavily from Yeongjin Jiang



#### Odds and Ends

• No lecture next Monday (5/27) – Memorial Day

• It is recommended to complete Part A by next lecture

#### Part-A Result

• You should get this OK before start exercise 8

dumbfork: OK (1.0s) Part A score: 5/5

#### • FAQ

- What if dumbfork halts?
  - Check if your sched\_yield()/env\_run() is implemented correctly
  - curenv must set as ENV\_RUNNABLE state if it is scheduled out...
- What if I have a syscall error?
  - Check if your implementation returns the return value of the syscall correctly
  - Check syscall arguments and orders
  - There always be syscalls to SYS\_getenvid and SYS\_cputs

## CAUTION: You Will See LOTS of Page Faults in Part B

• What should I do if I see a page fault?

set\_pgfault\_handler(handler);
cprintf("%s\n", (char\*)0xDeadBeef);
cprintf("%s\n", (char\*)0xCafeBffe);

- Check information related to the fault
  - Check tf\_eip (the origin of the fault)
  - Check fault\_va (read cr2, rcr2())
    - You can reason a lot from this address, e.g., 0xcafebffe?
  - → If it is 0, a null pointer dereference, check your impl!!!
  - Check error code (user/kernel, read/write, present?)
- Think about why this fault happens???

#### How Can I Get the Code for User Exec?

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- Read obj/user/xxxx.asm
- E.g., dumbfork:
  - You can match eip and the source code

	void duppage(env	id t dsta	env. void	*addr)		
	{			addi y		
	800040:	55		p	oush	%ebp
	800041:	89 e5		m	nov	%esp,%ebp
	800043:	56		p	oush	%esi
	800044:	53		p	oush	%ebx
	800045:	83 ec 20	Ð	S	sub	\$0x20,%esp
	800048:	8 <mark>b</mark> 75 08	8	m	nov	0x8(%ebp),%esi
	80004 <mark>b</mark> :	8 <mark>b 5d 0</mark> 0	С	m	nov	0xc(%ebp),%ebx
	int r;					
ב						
-	// This	is NOT \	what you s	hould do	o in yo	our fork.
	<b>if ((r</b>	= sys_pa	ge_alloc(d	l <mark>stenv,</mark> a	addr, P	PTE_P PTE_U PTE_W)) < 0)
	80004 <mark>e</mark> :	<b>c7_</b> 44_24	4 <mark>0</mark> 8 07 00	00 m	novl	\$0x7,0x8(% <mark>esp</mark> )
	800055 <b>:</b>	00				
	800056:	89 5 <mark>c</mark> 24	4 04	m	nov	%ebx,0x4(%esp)
	80005 <mark>a</mark> :	89 34 24	4	m	nov	%esi,(%esp)
	80005 <b>d</b> :	<mark>e8</mark> 81 0	d 00 00	c	all	800de3 <sys_page_alloc></sys_page_alloc>
>	800062:	85 <mark>c0</mark>		t	test	%eax,%eax
	800064:	79 20		j	jns	800086 <duppage+0x46></duppage+0x46>
	pan	ic("sys_	page_alloc	: %e", r	r);	
	800066:	89 44 24	4 0 <mark>c</mark>	m	nov	% <mark>eax</mark> ,0xc(% <mark>esp</mark> )
	80006 <mark>a</mark> :	c7 44 24	4 <mark>0</mark> 8 <mark>a0</mark> 12	80 m	novl	\$0x8012a0,0x8(% <mark>esp</mark> )
	800071:	00				
	800072:	<b>c7</b> 44 24	4 04 20 00	00 m	novl	\$0x20,0x4(% <mark>esp</mark> )
	800079:	00				
	80007 <mark>a</mark> :	c7 04 24	4 <mark>b3</mark> 12 80	00 m	novl	\$0x8012b3,(% <mark>esp</mark> )
	800081:	e8 24 02	2 00 00	c	all	8002aa <_panic>
	<b>if ((r</b>	= sys_pa	ge_map(dst	env, add	lr, 0,	UTEMP, PTE_P PTE_U PTE_W)) < 6
	800086:	c7 44 24	4 10 07 00	00 m	novl	\$0x7,0x10(%esp)
	80008 <mark>d</mark> :	00				
	80008e:	c7 44 24	4 Oc 00 00	40 m	novl	\$0x400000,0xc(%esp)
	800095:	00				

## **Debugging Tips**

 Check your traps. Recommend to print out some trap information whenever you got a trap...

```
static void
trap dispatch(struct Trapframe *tf)
    // Handle processor exceptions.
    // LAB 3: Your code here.
    uint32 t envid;
    if (curenv == NULL) envid = 0;
    else envid = curenv->env id;
    if (tf->tf trapno == T SYSCALL) {
        cprintf("Syscall from %p %s'(%p, %p, %p, %p, %p) from "
                "eip %p\n",
                envid,
                stringtbl[tf->tf regs.reg eax],
                tf->tf regs.reg edx,
                tf->tf regs.reg ecx,
                tf->tf regs.reg ebx,
                tf->tf regs.reg edi,
                tf->tf regs.reg esi,
                tf->tf eip);
    else if (tf->tf trapno == T PGFLT) {
        cprintf("Page fault from %p from va %p eip %p\n",
                envid,
                rcr2(), tf->tf eip);
    else {
        cprintf("Trap from %p number %d from eip %p\n",
                envid,
                tf->tf trapno, tf->tf eip);
```

## Debugging Tips

 Check your traps. Recommend to print out some trap information whenever you got a trap...

```
static void
trap_dispatch(struct Trapframe *tf)
{
    // Handle processor exceptions.
    // LAB 3: Your code here.
    uint32_t envid;
    if (curenv == NULL) envid = 0;
    else envid = curenv->env_id;
    if (tf->tf_trapno == T_SYSCALL) {
        cprintf("Syscall from %p %s(%p, %p, %p, %p, %p) from "
            "eip %p\n",
            envid,
            stringtbl[tf->tf_regs.reg_eax],
            tf->tf_regs.reg_edx,
            tf->tf_regs.reg_ecx,
            tf->tf_regs.reg_ecx,
            tf->tf_regs.reg_ecx,
```

[00000000] new env 00001000

Syscall from 0x1000 SYS\_getenvid(0x0, 0x0, 0x0, 0x0, 0x0) from eip 0x800bdf Syscall from 0x1000 SYS\_cputs(0xeebfde88, 0x27, 0x0, 0x0, 0x0) from eip 0x800b4f I am the parent. Forking the child... Syscall from 0x1000 SYS\_page\_alloc(0x1000, 0xeebff000, 0x7, 0x0, 0x0) from eip 0x800c23 Syscall from 0x1000 SYS\_env\_set\_pgfault\_upcall(0x0, 0x8012b9, 0x0, 0x0, 0x0) from eip 0x800d6f Syscall from 0x1000 SYS\_exofork(0x0, 0x8012b9, 0x0, 0x0, 0x0) from eip 0x800f77 [00001000] new env 00001001 Syscall from 0x1000 SYS\_page\_map(0x0, 0x200000, 0x1001, 0x200000, 0x805) from eip 0x800c76 Syscall from 0x1000 SYS\_page\_map(0x0, 0x200000, 0x0, 0x0, 0x805) from eip 0x800c76 Syscall from 0x1000 SYS\_page\_map(0x0, 0x200000, 0x0, 0x200000, 0x805) from eip 0x800c76

#### Recap: Data Race Example

- counter += value
  - edx = value;
  - eax = counter;
  - eax = edx + eax;
  - counter = eax;
- Assume counter = 0 at start



#### Counter = 1, not 2!!!

#### **Recap: Mutex**

- Lock
  - Prevent others enter the critical section
- Unlock
  - Release the lock, let others acquire the lock

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- counter += value
  - lock()
  - edx = value;
  - eax = counter;
  - eax = edx + eax;
  - counter = eax;
  - unlock()

Thread 1	Thread 2
Critical Section	
lock()	
edx = value	Critical Section
eax = counter	
eax = edx + eax	
counter = eax	IOCK()
unlock()	
Critical Section	edx = value
lock()	eax = counter eax = edx + eax
	unlock()
edx = value	
eax = counter	
eax = edx + eax	
counter = eax	
unlock()	

#### Spinlock Examples

- unzip lock-example-master.zip
- Run 30 threads, each count upto 10000
- Build code
  - \$ make

os2 ~/cs444/s21/lock-example-master 146% make gcc -o lock lock.c -std=c99 -g -Wno-impli<u>c</u>it-function-declaration -O2 -lpthread

#### Summary

- 5 Lock implementations
  - Naïve lock (bad\_lock, not working)
  - xchg lock (test-and-set, slow)
  - cmpxchg lock (a fake test and test-and-set, still slow)
  - Software test and hardware test-and-set (fast!)
  - Hardware test-and-set with exponential backoff (faster!)
- Performance check
  - Total execution time
  - L1-dcache-load-misses
  - Compare the performance to pthread\_mutex

#### lock-example

```
if (LLL_MUTEX_TRYLOCK (mutex) != 0)
                                                                          If the lock variable is not 0
  ł
    int cnt = 0;
    int max_cnt = MIN (max_adaptive_count (),
                          mutex->__data.__spins * 2 + 10);
                                                                     Spins * 2 + 10... exp backoff!
    do
         if (cnt++ >= max cnt)
             LLL MUTEX_LOCK (mutex);
             break;
                                                             #define atomic_spin_nop() __asm ("pause")
         atomic spin nop ();
      }
                                                             Check if the lock variable is 0...
    while (LLL MUTEX TRYLOCK (mutex) != 0);
```

#### Lock is Slow

- Run While() internally
- Can block other threads
- We need to determine when and where to use lock





#### When Do We Need to Use a Lock?

- Write must be finished before the next load
- Many writers and one reader
  - Yes... many writers..
- Two writers and two readers
  - Yes, two writers...
- One writer and many readers
  - Not always if there is only one writer



#### Where Do We Need to Put a Lock?

• What will happen if a critical section is too big?

![](_page_14_Figure_2.jpeg)

#### Wasting time for waiting for all

#### **Small Critical Sections**

![](_page_15_Figure_1.jpeg)

![](_page_15_Figure_2.jpeg)

Fast, but developer must take care of splitting critical sections..

#### **General Practice**

- Use lock only if it is required
  - Determine the case when you do not need a lock
    - Atomic read
    - Only one writer
- Use a small critical section
  - Critical section prohibits concurrent execution
  - Determine where do we share a variable
  - Wrap only the code that updates the shared variable
- Looks simple, but sometimes it's difficult

#### **Concurrency Bugs**

- Code does not have a bug when it runs with single thread could have a bug when it runs with multiple threads
  - Multiple cores, etc.
- What are the types of concurrency bugs?
  - Atomicity
  - Ordering
  - Deadlock

#### Atomicity

#### Read

```
Thread 1::
1
    if (thd->proc_info) { Time of check
2
3
       . . .
      fputs(thd->proc_info, ...); Time of use
4
5
       . . .
    }
6
                                              Time-of-check-to-time-of-use bug
7
    Thread 2::
8
                                               ΤΟCΤΤΟU
    thd->proc_info = NULL;
9
              Write!
```

#### Atomicity: Use Lock

![](_page_19_Figure_1.jpeg)

#### Ordering: Mozilla – Order 1

```
Thread 1::
1
    void init() {
2
3
         . . .
        mThread = PR_CreateThread(mMain, ...);
4
5
        . . .
    }
6
7
    Thread 2::
8
    void mMain(...) {
9
10
          . . .
         mState = mThread->State;
11
12
          . . .
13
     }
```

#### Ordering: Mozilla – Order 2

```
Thread 2::
8
    void mMain(...) {
9
10
          . . .
         mState = mThread->State;
11
                                         Not Initialized...
12
          . . .
13
    }
     Thread 1::
1
    void init() {
2
3
         . . .
        mThread = PR_CreateThread(mMain, ...);
4
5
        . . .
6
```

# How Can We Resolve the Ordering Issue?

5

6 7

8

10

11

12

13

14 15

16

17

18

19 20

21

22

23

24

25 26

27 23 28

29

- Use locks and conditional variables to force a specific ordering...
- pthread\_cond\_wait(cond, lock)
  - Set cond = 0
  - You will release the lock
  - Wait until cond == 1
  - Acquire the lock again
- pthread\_cond\_signal(cond)<sup>aits</sup>
  - cond = 1

Thread 1:: void init() { . . . mThread = PR\_CreateThread(mMain, ...); // signal that the thread has been created... pthread\_mutex\_lock(&mtLock); mtInit = 1;pthread\_cond\_signal(&mtCond); Sends Signal.. pthread mutex unlock (&mtLock); . . . Thread 2:: void mMain(...) { . . . // wait for the thread to be initialized... pthread\_mutex\_lock(&mtLock); while (mtInit == 0) pthread\_cond\_wait(&mtCond, &mtLock); pthread mutex unlock (&mtLock); mState = mThread->State; . . .

![](_page_23_Figure_0.jpeg)

![](_page_24_Picture_0.jpeg)

![](_page_25_Picture_0.jpeg)

![](_page_26_Picture_0.jpeg)

![](_page_27_Figure_0.jpeg)

![](_page_28_Picture_0.jpeg)

![](_page_29_Figure_0.jpeg)

![](_page_30_Figure_0.jpeg)

![](_page_31_Figure_0.jpeg)

#### How Can We Resolve Circular Dependency

Thread 1:
pthread\_mutex\_lock(L1);
pthread\_mutex\_lock(L2);

Thread 2:
pthread\_mutex\_lock(L2);
pthread\_mutex\_lock(L1);

![](_page_32_Picture_3.jpeg)

Thread 1:
pthread\_mutex\_lock(L1);
pthread\_mutex\_lock(L2);

Thread 2:
pthread\_mutex\_lock(L1);
pthread\_mutex\_lock(L2);

#### Circular Dependency

Thread 1:
pthread\_mutex\_lock(L1);
pthread\_mutex\_lock(L2);

Thread 2: pthread\_mutex\_lock(L2); pthread\_mutex\_lock(L1);

![](_page_33_Figure_3.jpeg)

## Non-Circular Dependency

Thread 1:
pthread\_mutex\_lock(L1);
pthread\_mutex\_lock(L2);

Thread 2:
pthread\_mutex\_lock(L1);
pthread\_mutex\_lock(L2);

![](_page_34_Figure_3.jpeg)