

# **ECE627**

## **Project 1**

**May 7, 2024**

1. Review a published paper on Nyquist-rate ADC or DAC and prepare an 8 minute power point presentation (roughly 8 to 12 slides) to be presented in class. The slides should clearly describe the application/specification, architecture (SAR, Flash, Pipelined, C-2C DAC etc and how it works), improvements proposed in the paper and final performance on the chip/proposed scheme. Consider doing literature search on IEEEXplore (<https://ieeexplore.ieee.org>) for papers published in any of the popular Journals and Conferences such as JSSCC, ISSCC, VLSI Symposium, CICC or ISCAS. You may use figures from the paper with proper citation.