

## REFERENCES

### General references:

-----

1. R. Schreier and G.C. Temes, Understanding Delta-Sigma Data Converters, IEEE Press/Wiley Interscience, 2005 - A general reference on the basic theory and design of delta-sigma ADCs and DACs.
2. S. Norsworthy, R. Schreier and G.C. Temes, eds., Delta-Sigma Data Converters, IEEE Press, 1997 - A detailed treatment of the analysis and design of delta-sigma data converters.
3. J..C. Candy and G.C. Temes, eds., Oversampling Delta-Sigma Data Converters, IEEE Press, 1991 - Contains a tutorial introduction to oversampled data converters, as well as a collection of key papers on the subject prior to 1991.

### References on delta-sigma ADCs:

-----

4. J.C. Candy, "A use of double integration in sigma-delta modulation," IEEE Trans. on Communications, vol.33, no.3, pp.249-258, March 1985 - An elegant qualitative analysis of the second-order delta-sigma modulator.
5. R.M. Gray, "Oversampled delta-sigma modulation," IEEE Trans. on Communications, vol. COM-35, pp. 481-489, May 1987 - Pioneering the exact mathematical treatment of the first-order single-bit delta-sigma modulator.
6. B.E. Boser and B.A. Wooley, "The design of sigma-delta modulation ADCs," IEEE J. Solid-State Circuits, vol. SC-23, pp. 1298-1308, December 1988 - Discusses the practical aspects of delta-sigma ADC analysis and design.
7. R. Schreier and B. Zhang, "Delta-sigma modulators employing continuous-time circuitry," IEEE Trans. on Circuits and Systems, I, vol.43, no.4, pp. 324-332, April 1996. - discusses design techniques for ADCs with continuous-time loop filters.

### References on delta-sigma DACs:

-----

8. N.S. Ssooch, J.W. Scott, T. Tanaka, T. Sugimoto and C. Kubomura, "18-bit

stereo D/A converter with integrated digital and analog filters," Convention of the Audio Engineering Society, New York, October 1991, Preprint 3113. - describes the design of a high-accuracy single-bit DAC.

9. I. Fujimori, A. Nogi and T. Sugimoto, "A multibit delta-sigma audio DAC with 120-dB dynamic range," IEEE J. Solid-State Circuits, vol. 35, no.8, pp. 1066-1073, August 2000. - Discusses the design of a delta-sigma DAC with a 31-level internal quantizer.

Reference on the computer-aided design of delta-sigma data converters:

-----  
10. R. Schreier, The Delta-Sigma Toolbox Version 7.1 (delsig), Dec. 2004, Software Toolbox and User's Manual. [Online]. Available: <http://www.mathworks.com/matlabcentral/fileexchange/> - A very useful tool for the analysis and design of the most important delta-sigma modulator systems.